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February 9, 2004

## MAIL STOP PATENT APPLICATION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Re:

Application of Junichi YAMADA

SEMICONDUCTOR MEMORY HAVING A DEFECTIVE MEMORY CELL

RELIEVING CIRCUIT

Assignee: NEC CORPORATION

Our Ref: Q79663



This is a request for a Divisional Application of pending prior Application No. 10/211,378 (Confirmation No. 1428) filed August 5., 2002 of Junichi YAMADA entitled SEMICONDUCTOR MEMORY HAVING A DEFECTIVE MEMORY CELL RELIEVING CIRCUIT.

This application is being filed under 37 C.F.R. § 1.53(b). Enclosed is a specification, including the claims and abstract, 24 sheets of drawings, and a copy of the Declaration as filed in the prior application.

The prior application is assigned to Group Art Unit 2818.

## Priority is claimed from:

Country

Application No

Filing Date

**JAPAN** 

2001-236638

August 3, 2001

The priority document was filed in parent Application No. 10/211,378.

The Government filing fee is calculated as follows:

Total claims

Independent claims

\$172.00

Base Fee

\$86.00 =

\$770.00

TOTAL FILING FEE

\$1032.00



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A check for the statutory fee of \$1032.00 is attached. The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this transmittal letter is attached.

The application is timely filed.

Respectfully submitted,

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